Pulse I/O and Counter Modules

Although Q Series I/O modules are designed to offer very fast responses to input signals, some applications need a shorter response than these modules can offer. In these cases, use the QI60 interrupt module. This offers response times as rapid as 50 microseconds for very fast event capture. For more sophisticated applications, the QD60P8-G offers isolated input capability together with averaging, scaling and sampling functions.

Model Number		Q160							
Stocked Item		<u>s</u>							
Certification		UL • cUL • CE							
Number of Input Points			16 points						
Rated Input Volta	ge		24VDC (+20/-15%, ripple ra	tio within 5%)					
Rated Input Curre	ent		Approx. 6mA						
ON Voltage/ON C	urrent		19V or higher/3mA or highe	er					
OFF Voltage/OFF	Current		11V or lower/1.7mA or lowe	11V or lower/1.7mA or lower					
	Set Value ((*1)	0.1	0.2	0.4	0.6	1		
Deenenee Time	ON – OFF	Тур	0.05	0.15	0.30	0.55	1.05		
(ms)		Max	0.10	0.20	0.40	0.60	1.20		
(1113)	OFF – ON	Тур	0.15	0.20	0.35	0.60	1.10		
		Max	0.20	0.30	0.50	0.70	1.30		
Common Termina	al Arrangem	ent	16 points/common (common terminal: TB17)						
I/O Device Points	Occupied		16 points						
External Connections		18-point terminal block (M3 x 6 screws)							
Applicable Crimping Terminal		R1.25-3 (sleeved crimping terminals cannot be used)							
5VDC Internal Current Consumption		60 mA (TYP. all points ON)							
Weight (kg)			0.20						
Base Unit Slots O	ccupied		1						

Note 1: Set via software.

Isolated Interrupt Module

Model Numbe	er	QD60P8-G						I	
Stocked Item		S							
Certification		CE							
Counting Speed Switch Settings		30kpps	10kpps	1kpps	100pps	50pps	10pps	1pps	0.1pps
Number of Channels		8 channels							
Count Input	Phase	1-phase input							
Signal	Signal Level	5VDC / 12 to 24V	DC						
	Counting Speed (Max.)	30kpps	10kpps	1kpps	100pps	50pps	10pps	1pps	0.1pps
	Count Range	Sampling pulse n Input pulse value	npling pulse number: 16-bits binary values (0 to 32767); Accumulating count value: 32-bits binary values (0 to 99999999) ut pulse value: 32-bits binary values (0 to 2147483647))
	Count Type	Linear counter m	Linear counter method, Ring counter method						
Counter (*1)	Minimum Count Pulse Width (Duty Ratio 50%)	33.4 µ s	100ms 50 50 ms ms	1ms 0.5 0.5 ms ms	10ms	20ms 10 10 ms ms	100ms 50 50 ms ms		
Connected Terminal		18 points terminal block							
I/O Device Points Occupied		32 points							
Applicable Solderless Terminals		R1.25-3 (A solderless terminals with sleeves cannot be used)							
Internal Curre	ent Consumption (5VDC)	0.58A							
Weight (kg)		0.17							
Base Unit Slo	ts Occupied	1						1	

Note 1: Counting speed is affected by pulse rise and fall time. Note that if a pulse that has a large rise and/or fall time is counted, a miscount may occur.

High Speed Counter Modules

These modules provide a capability for the CPUs to sense high frequency pulse trains as would be found in motion control and similar applications. Typically these modules would be linked to encoders to provide a closed loop of position sensing on a motion axis.

Model Numbe	er	QD62-H01	QD62-H02			
Stocked Item		-	-			
Certification		UL • CLL • CE				
Number of Oc	cupied I/O point	16 I/O points				
Number of Ch	nannels	2 channels				
Count Input	Phase	1-phase input, 2-phase input				
Signal	ON / OFF Characteristics	5/12/24VDC, 2 to 5mA				
	Counting Speed (Max) (*1)	1-phase input 50kPPS; 2-phase input 50kPPS	1-phase input 10kPPS; 2-phase input 7kPPS			
	Counting Range	32-bit signed binary (-2147483648 to 2147483647)				
	Туре	UP/DOWN Preset counter + Ring counter function				
Counter	Minimum Count Pulse Width (Duty Ratio 50%)	(1-phase input)	$\begin{array}{ c c c c c } \hline & & & & & & & & & & & & & & & & & & $			
External Input	Rated Input Voltage	5/12/24VDC, 2 to 5mA				
	Comparison Bange	32-hit signed hinary				
Comparison	Comparison System	Setting value < Count value Setting value = Count value Setting value > Count value				
Output	Number of Points	2 points/channel				
	Output Rating	Transistor (sink type)				
External Supply Power		12/24VDC 0.5A/point; 2A/common				
I/O Device Po	ints Occupied	16 points (I/O assignment: Intelligent 16 points)				
5VDC Interna	I Current Consumption (A)	0.30				
Weight (kg)		0.11				
Base Unit Slo	ots Occupied	1				

Note 1: Counting speed is affected by pulse rise and fall time. Possible counting speeds are shown in the following table. Note that a miscount may occur if the D62-H01 counts a pulse larger than t=50 µs. In this case, use the QD62-H02.

Model Numbe	ir	QD62	QD62E	QD62D	QD63P6		
Stocked Item		S	S	S	-		
Certification		UL • CLL • CE					
Compatible E	ncoder Types (*2) (*3)	Open collector type/CMOS	Open collector type/CMOS	Line driver type	Open collector type/CMOS		
Counting Speed Switch Setting		200k (100k to 200kPPS) 100k (10k to 100kPPS) 10k (10kPPS max.)		500k (200k to 500kPPS) 200k (100k to 200kPPS) 100k (10k to 100kPPS) 10k (10kPPS max.)	200k (100k to 200kPPS) 100k (10k to 100kPPS) 10k (10kPPS max.)		
Number of Ch	annels	2 channels			6 channels		
	Phase	1 phase input, 2 phase input					
Count Innut	Rated Input Voltage	5/12/24VDC (positive or negative common)		EIA Standard RS-422-A	6.4 to 11.5 mA at 5VDC		
Signal	ON / OFF Characteristics	5/12/24V; 2 to 5mA		Differential line driver level (*1)	0.4 to 11.5 mA at 5700		
orginar	Counting Range	32-bit designated binary (-214748					
	Туре	UP/DOWN preset counter + ring co					
External	Rated Input Voltage	5/12/24VDC (positive or negative of	common)	5/12/24V (*2)	5V		
Input	ON / OFF Characteristics	5/12/24V; 2 to 5mA	6.4 to 11.5mA				
	Comparison Range	32-bit designated binary (-2147483648 to 2147483647)					
	Comparison System	Set value < count value, set value =					
Comparison	Number of Points	2 points/channel			Internal I/O		
Output	Output Rating	Transistor (Sink) 12/24VDC 0.5A/ point 2A/common	Transistor (Source) 12/24VDC 0.1A/point 0.4A/common	Transistor (Sink) 12/24VDC 0.5A/ point 2A/common	-		
	External Supply Power	Voltage range: 10.2 to 30V, curren	-				
I/O Device Points Occupied		16 points (I/O assignment: 16 intelligent points)			32 points (I/O assignment: 32 intelligent points)		
5VDC Interna	Current Consumption (A)	0.30	0.33	0.38	0.59		
Weight (kg)		0.11		0.12	0.15		
Base Unit Slo	ts Occupied	1					

Notes:

Japan Texas Instruments product model Am26LS31 or equivalent.
Insure encoder output voltages are compatible with the module's input specifications.

3. TLL output type encoders cannot be used with the QD62, QD62E, and QD62D.

High Speed Counter Modules

QD62-H01

Counting Speed Switch Setting	1 Phase Input	2-Phase Input
t=5µs or less	50PPS	
t=50µs	5kPPS	
t=500µs	-	

QD62-H02

Counting Speed Switch Setting	1 Phase Input	2-Phase Input
t=5µs or less	10kPPS	7kPPS
t=50µs	-	
t=500µs	500PPS	250PPS

QD62

Counting Speed Switch Setting	200kPPS	100kPPS	10kPPS
Rise/Fall time	Both Phases 1	and 2	
t=1.25µs or less	200kPPS	100kPPS	10kPPS
t=2.5µs or less	100kPPS	100kPPS	10kPPS
t=25µs or less	-	10kPPS	10kPPS
t=500µs	-	-	500PPS

QD62E

Counting Speed Switch Setting	200kPPS	100kPPS	10kPPS
Rise/Fall time	Both Phases 1	and 2	
t=1.25µs or less	200kPPS	100kPPS	10kPPS
t=2.5µs or less	100kPPS	100kPPS	10kPPS
t=25µs or less	-	10kPPS	10kPPS
t=500µs	-	-	500PPS

QD62D

Counting Speed Switch Setting	500kPPS	200kPPS	100kPPS	10kPPS
Rise/Fall time	Both Phases	1 and 2		
t=0.5µs or less	500kPPS	200kPPS	100kPPS	10kPPS
t=1.25µs or less	200kPPS	200kPPS	100kPPS	10kPPS
t=2.5µs or less	-	100kPPS	100kPPS	10kPPS
t=25µs or less	-	-	10kPPS	10kPPS
t=500µs	-	-	-	500PPS

Note: Inputting a waveform with a long rise/fall time may cause a false input. Use a waveform within the permissible rise/fall time.

QD63P6

Counting Speed Switch Setting	200kPPS	100kPPS	10kPPS
Rise/Fall time	Both Phases 1	and 2	
t=1.25µs or less	200kPPS	100kPPS	10kPPS
t=2.5µs or less	100kPPS	100kPPS	10kPPS
t=25µs or less	-	10kPPS	10kPPS
t=500µs	-	-	500PPS

Multi-Function Counter/Timer Module

		QD65PD2				
Wodel Number		Differential input	DC Input			
Stocked Item		S				
Certification		UL • cUL • CE				
Number of Occu	pied I/O Points	32 points (I/O assignment: Intelligent, 32 points)				
Number of Channels		2 channels				
Counting	1 Multiple	10kpps/100kpps/200kpps/500kpps/ 1Mpps/2Mpps				
Speed Switch	2 Multiples	10kpps/100kpps/200kpps/500kpps/ 1Mpps/2Mpps/4Mpps	10kpps/100kpps/200kpps			
Setting (*1)	4 Multiples	10kpps/100kpps/200kpps/500kpps/ 1Mpps/2Mpps/ 4Mpps/8Mpps				
Count Innut	Phase	1-phase input (1 multiple/2 multiples), 2-phase input (1 multiple/2 mu	Itiples/4 multiples), CW/CCW			
Signal	Signal Level (øA, øB)	EIA Standards RS-422-A, differential line driver level (AM26LS31 [manufactured by Texas Instruments] or equivalent)	5/12/24VDC, 7 to 10mA			
	Counting Speed (Max) (*2, *3)	8Mpps (4 multiples of 2 phases)	200kpps			
	Counting Range	32-bit signed binary (-2147483648 to 2147483647)				
	Format	Count, subtraction count; Linear counter format, ring counter format; I	Preset/replace function, latch counter function			
		1-phase input (1 multiple/2 multiples), CW/CCW	1-phase input (1 multiple/2 multiples), CW/CCW			
		$\underbrace{\begin{array}{c} 0.5 \ \mu a \\ 0.25 \ \mu a$	(Minimum pulse width in			
Counter			2 manufield (2 multiples (4 multiples)			
Counter	Minimum Count Pulse Width (Duty Ratio 50%)	2-phase input (1 multiple/2 multiples/4 multiples)	2-phase input (1 multiple/2 multiples/4 multiples)			
	Comparison Banne	32-bit signed hinary	· · · · · · · · · · · · · · · · · · ·			
	Comparison System	Setting value < Count value: Setting value = Count value: Setting value	> Count value			
Comparison	In-Range Output	Setting value (lower limit value) < Count value < Setting value (upper limit value)				
Output	Not-In-Range Output	Count value < Setting value (lower limit value), Setting value (upper limit value) < Count value				
	Interrupt	Equipped with a coincidence detection interrupt function	/			
	Phase Z	EIA Standards RS-422-A, differential line driver level (AM26LS31 [manufactured by Texas Instruments] or equivalent): 2 points	5/12/24VDC, 7 to 10mA: 2 points			
External Input	Function	5/12/24VDC, 7 to 10mA: 2 points				
	Latch Counter	5/12/24VDC, 7 to 10mA: 2 points				
	General Input	24 VDC, High Speed: 7 to 10mA, 2 points, Low Speed: 3mA, 4 points				
External	Coincidence Output (High Speed)	Transistor (sink type) output: 2 points 12/24VDC 0.1A/point, 0.8A/com	mon			
Output	(Low Speed)	Transistor (sink type) output: 6 points 12/24VDC 0.1A/point, 0.8A/com	mon			
	Measurement Item	Iransistor (sink type) output: 8 points 12/24VDC 0.1A/point, 0.8A/common				
Pulse Measurement	Measurement Resolution	100ns				
mousurement	Measurement Points	2 points/channel				
	Number of Output Points	8 points (max. 16 steps/point)				
0	Control Cycle	1ms				
Cam Switch	Difference Between Each Output Duration	100µs or less				
	in a Channel Coincidence Output (High Speed)	DC and up to 200kHz				
Frequency Bange	Coincidence Output (Low Sneed)	DC and up to 2kHz				
	Duty Ratio	Any ratio (Can be set by 0.1µs)				
5VDC Internal Current Consumption (A)		0.23				
Applicable Wire	e Size	0.3mm ² (22 AWG) (A6CON1 and A6CON4), 0.088mm ² to 0.24mm ² (24	to 28 AWG) (A6CON2)			
Applicable Con Wiring (Sold Se	nector for External eparately)	A6CON1, A6CON2, A6CON4				
External Dimen	sions (H x W x D) mm	98 x 27.4 x 90				
Weight (kg)		0.15				
Base Unit Slots Occupied		1				

Notes:

Notes:
Counting speed switch setting can be done using the switch setting.
Counting speed switch setting can be done using the switch setting.
Note that the count may be done incorrectly by inputting pulses whose phase difference is small between the phase A pulse and phase B pulse. To check the input waveform of the phase A pulse and phase B pulse, or to check phase difference between the phase A pulse and phase B pulse, refer to User's Manual
The counting speed is affected by the pulse rise/fall time. The number of pulses that can be counted depending on the counting speed is listed below. Note that the count may be done incorrectly by counting

pulses with long rise/fall time.