## Pulse I/O and Counter Modules

Although Q Series I/O modules are designed to offer very fast responses to input signals, some applications need a shorter response than these modules can offer. In these cases, use the Ql60 interrupt module. This offers response times as rapid as 50 microseconds for very fast event capture. For more sophisticated applications, the QD60P8-G offers isolated input capability together with averaging, scaling and sampling functions.

| Model Number |  |  | Q160 |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Stocked Item |  |  | S |  |  |  |  |
| Certification |  |  | UL • CUL •CE |  |  |  |  |
| Number of Input Points |  |  | 16 points |  |  |  |  |
| Rated Input Voltage |  |  | 24VDC (+20/-15\%, ripple ratio within 5\%) |  |  |  |  |
| Rated Input Current |  |  | Approx. 6mA |  |  |  |  |
| ON Voltage/ON Current |  |  | 19 V or higher/3mA or higher |  |  |  |  |
| OFF Voltage/OFF Current |  |  | 11V or lower/1.7mA or lower |  |  |  |  |
| Response Time (ms) | Set Value (*1) |  | 0.1 | 0.2 | 0.4 | 0.6 | 1 |
|  | ON - OFF | Typ | 0.05 | 0.15 | 0.30 | 0.55 | 1.05 |
|  |  | Max | 0.10 | 0.20 | 0.40 | 0.60 | 1.20 |
|  | OFF - ON | Typ | 0.15 | 0.20 | 0.35 | 0.60 | 1.10 |
|  |  | Max | 0.20 | 0.30 | 0.50 | 0.70 | 1.30 |
| Common Terminal Arrangement |  |  | 16 points/common (common terminal: TB17) |  |  |  |  |
| I/O Device Points Occupied |  |  | 16 points |  |  |  |  |
| External Connections |  |  | 18-point terminal block (M3 $\times 6$ screws) |  |  |  |  |
| Applicable Crimping Terminal |  |  | R1.25-3 (sleeved crimping terminals cannot be used) |  |  |  |  |
| 5VDC Internal Current Consumption |  |  | 60 mA (TYP. all points ON) |  |  |  |  |
| Weight (kg) |  |  | 0.20 |  |  |  |  |
| Base Unit Slots Occupied |  |  | 1 |  |  |  |  |

Note 1: Set via software.

## Isolated Interrupt Module



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## High Speed Counter Modules

These modules provide a capability for the CPUs to sense high frequency pulse trains as would be found in motion control and similar applications. Typically these modules would be linked to encoders to provide a closed loop of position sensing on a motion axis.


Note 1: Counting speed is affected by pulse rise and fall time. Possible counting speeds are shown in the following table. Note that a miscount may occur if the D62-H01 counts a pulse larger than $\mathrm{t}=50 \mu \mathrm{~s}$. In this case, use the QD62-H02

| Model Number |  | QD62 | QD62E | QD62D | QD63P6 |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Stocked Item |  | S | S | S | - |
| Certification |  | UL • CUL • CE |  |  |  |
| Compatible Encoder Types (*2) (*3) |  | Open collector type/CMOS | Open collector type/CMOS | Line driver type | Open collector type/CMOS |
| Counting Speed Switch Setting |  | 200k (100k to 200kPPS) <br> 100k (10k to 100kPPS) 10k (10kPPS max.) |  | 500k (200k to 500kPPS) <br> 200k (100k to 200kPPS) <br> 100k (10k to 100kPPS) <br> 10k (10kPPS max.) | 200k (100k to 200kPPS) 100k (10k to 100kPPS) 10k (10kPPS max.) |
| Number of Channels |  | 2 channels |  |  | 6 channels |
| Count Input Signal | Phase | 1 phase input, 2 phase input |  |  |  |
|  | Rated Input Voltage | 5/12/24VDC (positive or negative common) |  | EIA Standard RS-422-A Differential line driver level (*1) | 6.4 to 11.5 mA at 5VDC |
|  | ON / OFF Characteristics | 5/12/24V; 2 to 5mA |  |  | 6.4 to 11.5 mA at sVoc |
|  | Counting Range | 32-bit designated binary (-2147483648 to 2147483647) |  |  |  |
|  | Type | UP/DOWN preset counter + ring counter functions |  |  |  |
| External Input | Rated Input Voltage | 5/12/24VDC (positive or negative common) |  | 5/12/24V (*2) | 5 V |
|  | ON / OFF Characteristics | 5/12/24V; 2 to 5mA |  |  | 6.4 to 11.5 mA |
| Comparison Output | Comparison Range | 32-bit designated binary (-2147483648 to 2147483647) |  |  |  |
|  | Comparison System | Set value < count value, set value = count value, set value>count value |  |  |  |
|  | Number of Points | 2 points/channel |  |  | Internal I/0 |
|  | Output Rating | Transistor (Sink) 12/24VDC 0.5A/ point 2A/common | Transistor (Source) 12/24VDC <br> $0.1 \mathrm{~A} /$ point $0.4 \mathrm{~A} / \mathrm{common}$ | Transistor (Sink) 12/24VDC 0.5A/ point 2A/common | - |
|  | External Supply Power | Voltage range: 10.2 to 30V, curren | t consumption: 8mA (typ @ 24 V |  | - |
| I/O Device Points Occupied |  | 16 points (I/0 assignment: 16 intelligent points) |  |  | 32 points (I/O assignment: 32 intelligent points) |
| 5VDC Internal Current Consumption (A) |  | 0.30 | 0.33 | 0.38 | 0.59 |
| Weight (kg) |  | 0.11 |  | 0.12 | 0.15 |
| Base Unit Slots Occupied |  | 1 |  |  |  |

## Notes

1. Japan Texas Instruments product model Am26LS31 or equivalent
2. Insure encoder output voltages are compatible with the module's input specifications.
3. TLL output type encoders cannot be used with the QD62, QD62E, and QD62D

High Speed Counter Modules

QD62-H01

| Counting Speed Switch Setting | 1 Phase Input | 2-Phase Input |
| :--- | :--- | :--- |
| $t=5 \mu \mathrm{~s}$ or less | 50 PPS |  |
| $\mathrm{t}=50 \mu \mathrm{~s}$ | 5 kPPS |  |
| $\mathrm{t}=500 \mu \mathrm{~s}$ | - |  |

QD62-H02

| Counting Speed Switch Setting | 1 Phase Input | 2-Phase Input |
| :--- | :--- | :--- |
| $t=5 \mu \mathrm{~s}$ or less | 10 kPPS | 7kPPS |
| $\mathrm{t}=50 \mu \mathrm{~s}$ | - |  |
| $\mathrm{t}=500 \mu \mathrm{~s}$ | 500 PPS | 250PPS |

QD62

| Counting Speed Switch Setting | 200kPPS |  |  |
| :--- | :--- | :--- | :--- |
| Rise/Fall time | 100kPS |  | 10kPPS |
| $\mathrm{t}=1.25 \mu \mathrm{~s}$ or less | 200 kPPS | 100 kPPS | 10 kPPS |
| $\mathrm{t}=2.5 \mu \mathrm{~s}$ or less | 100 kPPS | 100 kPPS | 10 kPPS |
| $\mathrm{t}=25 \mu \mathrm{~s}$ or less | - | 10 kPPS | 10 kPPS |
| $\mathrm{t}=500 \mu \mathrm{~s}$ | - | - | 500 PPS |

QD62E

| Counting Speed Switch Setting | 200kPPS |  | 100kPS |
| :--- | :--- | :--- | :--- |
| 10kPPS |  |  |  |
| Rise/Fall time |  |  |  |
| $t=1.25 \mu \mathrm{~s}$ or less Phases 1 and 2 |  |  |  |
| $\mathrm{t}=2.5 \mu \mathrm{~s}$ or less | 200 kPPS | 100 kPPS | 10 kPPS |
| $\mathrm{t}=25 \mu \mathrm{~s}$ or less | 100 kPPS | 100 kPPS | 10 kPPS |
| $\mathrm{t}=500 \mu \mathrm{~s}$ | - | 10 kPPS | 10 kPPS |

QD62D

| Counting Speed Switch Setting | 500kPP | 200kPPS | 100kPS | 10kPPS |  |
| :--- | :--- | :--- | :--- | :--- | :---: |
| Rise/Fall time |  |  |  |  |  |
| $\mathrm{t}=0.5 \mu \mathrm{~s}$ or less Phases 1 and 2 |  |  |  |  |  |
| $\mathrm{t}=1.25 \mu \mathrm{~s}$ or less | 500 kPPS | 200 kPPS | 100 kPPS | 10 kPPS |  |
| $\mathrm{t}=2.5 \mu \mathrm{~s}$ or less | 200 kPPS | 200 kPPS | 100 kPPS | 10 kPPS |  |
| $\mathrm{t}=25 \mu \mathrm{~s}$ or less | - | 100 kPPS | 100 kPPS | 10 kPPS |  |
| $\mathrm{t}=500 \mu \mathrm{~s}$ | - | - | 10 kPPS | 10 kPPS |  |

Note: Inputting a waveform with a long rise/fall time may cause a false input. Use a waveform within the permissible rise/fall time.

## QD63P6

| Counting Speed Switch Setting | 200kPPS | 100kPPS | 10kPPS |
| :---: | :---: | :---: | :---: |
| Rise/Fall time | Both Phases 1 and 2 |  |  |
| $\mathrm{t}=1.25 \mu \mathrm{~s}$ or less | 200kPPS | 100kPPS | 10kPPS |
| $\mathrm{t}=2.5 \mu \mathrm{~s}$ or less | 100kPPS | 100kPPS | 10kPPS |
| $\mathrm{t}=25 \mu \mathrm{~s}$ or less | - | 10kPPS | 10kPPS |
| t=500 $\mathrm{s}^{\text {s }}$ | - | - | 500PPS |

## Multi-Function Counter/Timer Module

| Model Number |  | QD65PD2 |  |
| :---: | :---: | :---: | :---: |
|  |  | Differential input | DC Input |
| Stocked Item |  | S |  |
| Certification |  | UL • CUL • CE |  |
| Number of Occupied I/O Points |  | 32 points (I/O assignment: Intelligent, 32 points) |  |
| Number of Channels |  | 2 channels |  |
| Counting <br> Speed Switch <br> Setting (*1) | 1 Multiple | 10kpps/100kpps/200kpps/500kpps/ 1Mpps/2Mpps | 10kpps/100kpps/200kpps |
|  | 2 Multiples | 10kpps/100kpps/200kpps/500kpps/ 1Mpps/2Mpps/4Mpps |  |
|  | 4 Multiples | 10kpps/100kpps/200kpps/500kpps/ 1Mpps/2Mpps/ 4Mpps/8Mpps |  |
| Count Input Signal | Phase | 1-phase input (1 multiple/2 multiples), 2-phase input (1 multiple/2 multiples/4 multiples), CW/CCW |  |
|  | Signal Level (øA, øB) | EIA Standards RS-422-A, differential line driver level (AM26LS31 [manufactured by Texas Instruments] or equivalent) | 5/12/24VDC, 7 to 10mA |
| Counter | $\begin{aligned} & \text { Counting Speed (Max) } \\ & (* 2, * 3) \end{aligned}$ | 8Mpps (4 multiples of 2 phases) | 200kpps |
|  | Counting Range | 32-bit signed binary (-2147483648 to 2147483647 ) |  |
|  | Format | Count, subtraction count; Linear counter format, ring counter form | Preset/replace function, latch counter function |
|  | Minimum Count Pulse Width (Duty Ratio 50\%) | 1-phase input (1 multiple/2 multiples), CW/CCW | 1-phase input (1 multiple/2 multiples), CW/CCW <br> (Minimum pulse width in <br> 2 multiples of 1 phase: $2.5 \mu \mathrm{~s}$ ) |
|  |  | 2-phase input ( 1 multiple/2 multiples/4 multiples) <br> 4 multiples of 2 phases: $0.125 \mu \mathrm{~s}$ ) | 2-phase input (1 multiple/2 multiples/4 multiples) |
| Comparison Output | Comparison Range | 32-bit signed binary |  |
|  | Comparison System | Setting value < Count value; Setting value = Count value; Setting value > Count value |  |
|  | In-Range Output | Setting value (lower limit value) $\leq$ Count value $\leq$ Setting value (upper limit value) |  |
|  | Not-In-Range Output | Count value < Setting value (lower limit value), Setting value (upper limit value) < Count value |  |
|  | Interrupt | Equipped with a coincidence detection interrupt function |  |
| External Input | Phase Z | EIA Standards RS-422-A, differential line driver level (AM26LS31 [manufactured by Texas Instruments] or equivalent): 2 points | 5/12/24VDC, 7 to 10mA: 2 points |
|  | Function | 5/12/24VDC, 7 to 10mA: 2 points |  |
|  | Latch Counter | 5/12/24VDC, 7 to 10mA: 2 points |  |
|  | General Input | 24 VDC, High Speed: 7 to 10mA, 2 points, Low Speed: 3mA, 4 poi |  |
| External Output | Coincidence Output (High Speed) | Transistor (sink type) output: 2 points 12/24VDC 0.1A/point, 0.8A/common |  |
|  | Coincidence output (Low Speed) | Transistor (sink type) output: 6 points 12/24VDC 0.1A/point, 0.8A/common |  |
|  | General Output | Transistor (sink type) output: 8 points 12/24VDC 0.1A/point, 0.8A/common |  |
| Pulse <br> Measurement | Measurement Item | Pulse width (ON width/OFF width) |  |
|  | Measurement Resolution | 100ns |  |
|  | Measurement Points | 2 points/channel |  |
| Cam Switch | $\begin{array}{\|l} \hline \begin{array}{l} \text { Number of Output } \\ \text { Points } \end{array} \\ \hline \end{array}$ | 8 points (max. 16 steps/point) |  |
|  | Control Cycle | 1 ms |  |
|  | Difference Between Each Output Duration in a Channel | $100 \mu$ s or less |  |
| PWM Output <br> Frequency <br> Range | Coincidence Output (High Speed) | DC and up to 200 kHz |  |
|  | Coincidence Output (Low Speed) | DC and up to 2 kHz |  |
|  | Duty Ratio | Any ratio (Can be set by $0.1 \mu \mathrm{~s}$ ) |  |
| 5VDC Internal Current Consumption (A) |  | 0.23 |  |
| Applicable Wire Size |  | $0.3 \mathrm{~mm}^{2}$ (22 AWG) (A6CON1 and A6CON4), $0.088 \mathrm{~mm}^{2}$ to 0.24mm² (24 to 28 AWG) (A6CON2) |  |
| Applicable Connector for External Wiring (Sold Separately) |  | A6CON1, A6CON2, A6CON4 |  |
| External Dimensions (H x W x D) mm |  | $98 \times 27.4 \times 90$ |  |
| Weight (kg) |  | 0.15 |  |
| Base Unit Slots Occupied |  | 1 |  |

## Notes:

1. Counting speed switch setting can be done using the switch setting.
2. Note that the count may be done incorrectly by inputting pulses whose phase difference is small between the phase A pulse and phase B pulse. To check the input waveform of the phase A pulse and phase B pulse, or to check phase difference between the phase A pulse and phase B pulse, refer to User's Manual
3. The counting speed is affected by the pulse rise/fall time. The number of pulses that can be counted depending on the counting speed is listed below. Note that the count may be done incorrectly by counting pulses with long rise/fall time.

[^0]:    Note 1: Counting speed is affected by pulse rise and fall time. Note that if a pulse that has a large rise and/or fall time is counted, a miscount may occur.

